

UNIT -III: Programmable Digital Signal Processors: Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX processors, Pipeline operation of TMS320C54XX Processors.


UNIT –V: Interfacing Memory and I/O Peripherals to Programmable DSP Devices: Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

References/Text Books:
7. Digital Signal Processing Applications Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI
8. The Scientist and Engineer's Guide to Digital Signal Processing by Steven W. Smith, Ph.D.,

**Lecture Plan:** Unit-I & -II syllabus for MID-I, Unit-III & -IV syllabus for MID-II and Unit-V & -VI syllabus for MID-III examinations.

**Video Lectures (Web Links):**
1. 
2. 

**Study Materials (Web Links):**
1. 
2. 

**Problems & Solutions (Web Links):**
1. 
2. 

**Assignments (Web Links):**
1. 
2. 